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Design And Test (Frontiers In Electronic Testing)**

**By Andrei Pavlov;Manoj Sachdev**

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Summary: Pavlov, Andrei is the author of CMOS SRAM Circuit Design and Parametric Test in Nano-Scaled Technologies : Process-Aware SRAM Design and Test, published 2010  
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**Manoj Sachdev (Author of CMOS Sram Circuit Design -**

Manoj Sachdev is the author of Defectoriented Testing for Nanometric CMOS VLSI Circuits (0.0 avg rating, 0 ratings,

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